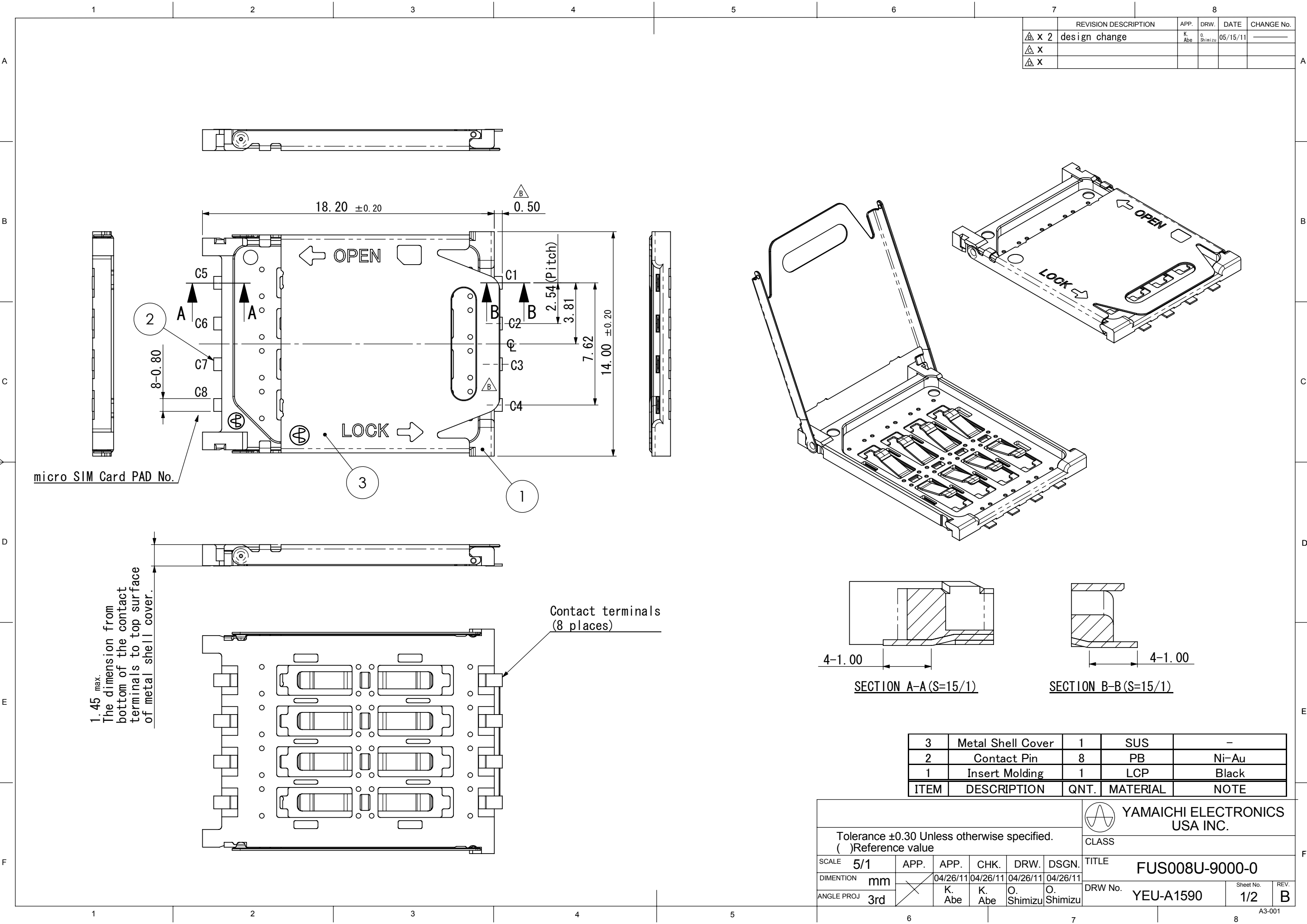
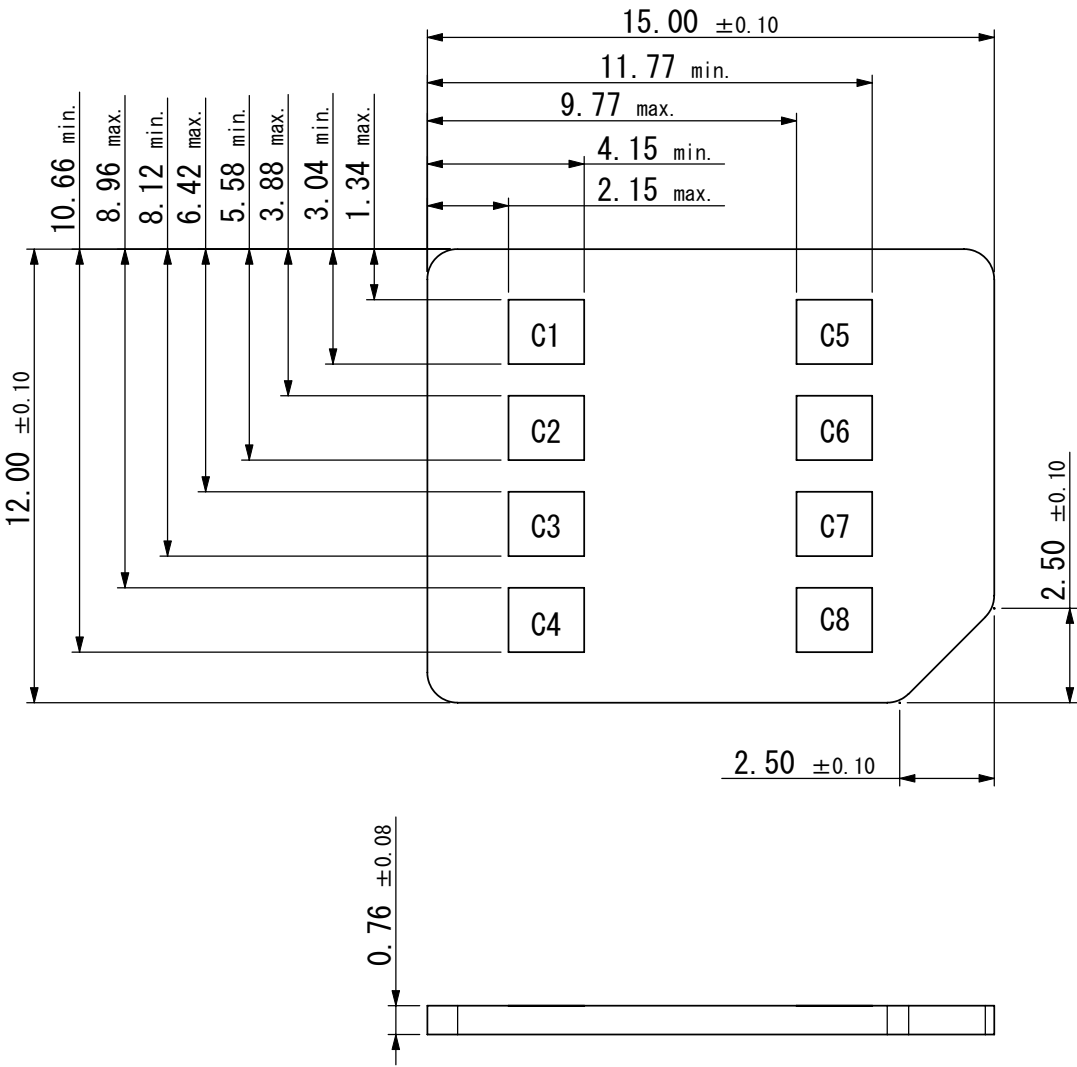
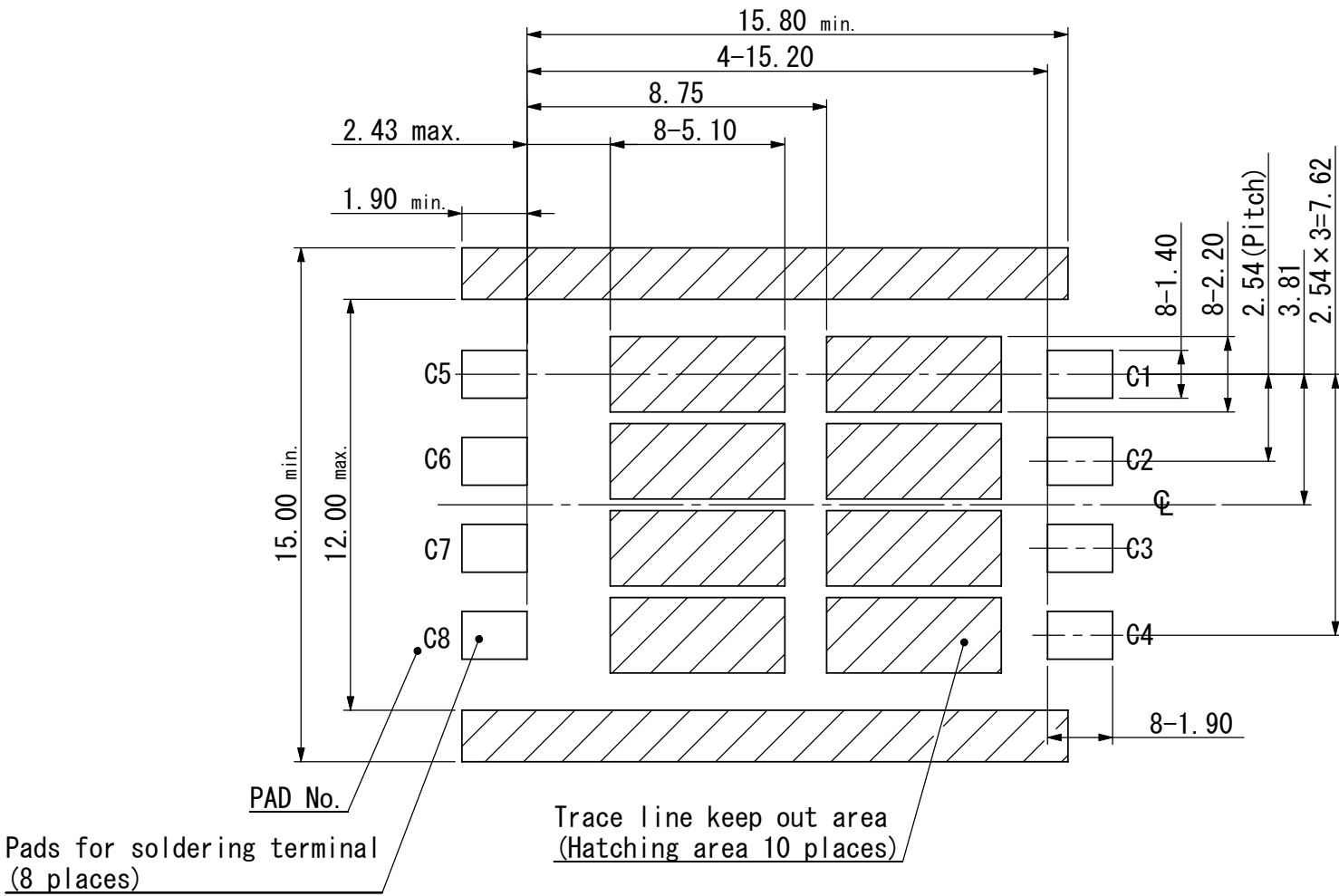




Recommended PCB Layout (Connector Mounting Side)

Tolerances ±0.05 unless otherwise specified



Applicable Card Size

Note

- 1. ϕ indicates the centerline of the connector with 14.00mm.
- 2. Coplanarity for contact terminals shall be 0.10mm maximum.
- 3. Pin Assignment

1	VCC(Supply voltage)
2	RST(Reset singnail)
3	CLT(Clock signal)
4	Reserved
5	GND(Ground)
6	VPP(Programining voltage)
7	I/O(Data input/output)
8	Reserved

Tolerance ±0.30 Unless otherwise specified. ()Reference value						YAMAICHI ELECTRONICS USA INC.	
CLASS						TITLE	
SCALE 5/1						FUS008U-9000-0	
DIMENTION mm						DRW No.	
ANGLE PROJ 3rd						YEU-A1590	
APP. APP. CHK. DRW. DSGN.						Sheet No.	
04/26/11 04/26/11 04/26/11 04/26/11						2/	
K. Abe K. Abe O. Shimizu O. Shimizu						REV.	
						A	